

In re application of

:

Junichi NAKA et al.

Confirmation No. 9592

Serial No. 10/784,929

Attn: BOX MISSING PARTS

Filed February 25, 2004

Attorney Docket No. 2004 0299A

STANDARD VOLTAGE GENERATION

CIRCUIT

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

THE COMMISSIONER IS AUTHORIZED TO CHARGE ANY DEFICIENCY IN THE FEES FOR THIS PAPER TO DEPOSIT ACCOUNT NO. 23-0975

Sir:

Pursuant to the provisions of 37 C.F.R. 1.56, 1.97 and 1.98, Applicants request consideration of (X) the references listed on the attached Form PTO-1449. A legible copy of each reference listed on the Form PTO-1449 is enclosed, except a copy is not provided for each U.S. Patent and U.S. Patent application publication (for U.S. National applications filed after June 30, 2003 and International applications that have entered U.S. National Stage after June 30, 2003).

1a. [X] This Information Disclosure Statement is submitted:

within three months of the filing date (or of entry into the National Stage) of the above-entitled application, or

before the mailing of a first Office Action on the merits or the mailing of a first Office Action after the filing of an RCE,

and thus no certification and/or fee is required.

1b. [] This Information Disclosure Statement is submitted

after the events of above paragraph 1a and prior to the mailing date of a final Office Action or a Notice of Allowance or an action which otherwise closes prosecution in the application, and thus:

- (1) [] the certification of paragraph 2 below is provided, or
- (2) [] the fee of \$180.00 specified in 37 CFR 1.17(p) is enclosed.
- 1c. [] This Information Disclosure Statement is submitted:

after the mailing date of a final Office Action or Notice of Allowance or action which otherwise closes prosecution in the application, and prior to payment of the issue fee, and thus:

the certification of paragraph 2 below is provided, and

the fee of \$180.00 specified in 37 CFR 1.17(p) is enclosed.

- 2. It is hereby certified
 - a. [] that each item of information contained in this Information Disclosure

 Statement was first cited in any communication from a foreign patent office in a

 counterpart foreign application not more than three months prior to the filing of
 the Statement, or
 - b. [] that no item of information contained in the Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application and, to the knowledge of the person signing the certification after making reasonable inquiry, was known to any individual designated in §1.56(c) more than three months prior to the filing of the Statement.
- 3. [] Consideration of the following list of additional information (including any copending or abandoned U.S. application, prior uses and/or sales, etc.) is requested.
- 4. For each non-English language reference listed on the attached form PTO-1449, reference is made to:
 - a. [X] a full or partial English language translation submitted herewith,

- b. [] a foreign patent office search report (in the English language) submitted herewith,
- c. [] the concise explanation contained in the specification of the present application at page,
- d. [] the concise explanation set forth in the attached English language abstract,
- e. [] the concise explanation set forth below or on a separate sheet attached to the reference:
- 5. [] A foreign patent office search report citing one or more of the references is enclosed.

Respectfully submitted,

Junichi NAKA et al.

Leffrey R. Filipek

Registration No. 41,471 Attorney for Applicants

JRF/fs

Washington, D.C. 20006-1021 Telephone (202) 721-8200 Facsimile (202) 721-8250 June 15, 2004

Sheet 1 of 1										
FORM PTO 1449 (<i>n</i>		ATTY DOCKET NO. SERIAL 2004_0299A 10/78-								
PAT LIST OF	APPLICANT Junichi NAKA et al.									
Date	FILING DATE February 25, 2004			GROUP 2816						
U.S. PATENT DOCUMENTS										
*EXAMINER INITIAL		DOCUMENT NUMBER	DATE		NAME CLASS			SUBCLASS FILING DATE IF APPROPRIATE		
SIPE	AA	5,892,381	4-6-99	Koif	man et al.					_
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OTHER DOCUMENT(S) (Including Author, Title, Date, Pertinent Pages, Etc.)										
	AO	"Standard Text Design of OP Amplifier Circuit", page 234-235								
	AP									
	AQ			·					 	
EXAMINER		DATE CONSIDERED								